



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants:	Hahn Vo	§	Art Unit:	2186
Serial No.:	10/039,045	§		
Filed:	December 31, 2001	§	Examiner:	Zhuo H. Li
For:	Low Latency Lock for Multiprocessor Computer System	§	Atty. Dkt. No.:	200301872-1 (HPC.0064US)

Mail Stop AF
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

AMENDMENT UNDER 37 C.F.R. § 1.312

Dear Sir:

Please amend the above-identified application as follows:

Amendments to the Claims are reflected in the listing of claims, which begins on page 2 of this paper.

Remarks/Arguments begin on page 12 of this paper.

Date of Deposit: May 19, 2005

I hereby certify under 37 CFR 1.8(a) that this correspondence is being deposited with the United States Postal Service as first class mail with sufficient postage on the date indicated above and is addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313.

Susan J. Scott
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